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	Steven Towle John Tang Gilroy Vandentop BEST AVAILABLE (High performance, low cost microelectronic circuit packs	j
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	TERMINAL DRAWINGS CLAIM DISCLAIMER Sheets Drwg. Figs. Drwg. Print Fig. Total Claims	S ALLOWED Print Claim for O.G.
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	has been discitatined: [Assistant Examiner] (Deta) The term of this patent shall not extend beyond the expiration date	
	of U.S Patient, No	UE FEE
	(Primary Examiner) (Date)	SALAMA
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